

AMENDMENTS TO THE SPECIFICATION:

Page 2, replace the paragraph beginning on line 17 with the following amended paragraph:

--When a shift clock is fixed, the LFSR repeatedly generates the same random number series at a predetermined cycle from a predetermined initial value (SEED). On the other hand, the pseudo-random number generator ~~described~~ described in Japanese Patent No. 2937919 cited above switches shift clocks using a predetermined output of the LFSR to cause variations in timing of a shifting operation, with the result that an apparent cycle becomes longer than the cycle determined by the order number of the LFSR.--